

ABSTRACT OF THE DISCLOSURE

According to an equalizing circuit and an equalizing method, a memory control unit receives an input of an input image data signal, a first memory stores the input image data signal after delaying it and a CPU designates at least any one of a main scan coordinate and a subscan coordinate to start equalizing of the input image data signal, a main scan size and a subscan size of the equalized block and skew values in a main scan direction and in a subscan direction of the equalized block. Then, a register setting unit holds the setting information which is designated by the CPU and an equalizing control unit performs the equalizing of the input image data signal at a certain timing independently of a skew value of the equalized block on the basis of the setting information held by the register setting unit and outputs the equalized image data signal. Thus, a second memory receives an input of the equalized image data signal from the equalizing control unit and holds it as an output image data signal and an output control unit outputs the output image data of the second memory.